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<u>L44</u>	112 and 120	19	<u>L44</u>
<u>L43</u>	112 and 119	55	<u>L43</u>
<u>L42</u>	112 and 118	55	<u>L42</u>
<u>L41</u>	112 and 117	6	<u>L41</u>
<u>L40</u>	112 and 116	10	<u>L40</u>
<u>L39</u>	112 and 115	16	<u>L39</u>
<u>L38</u>	112 and 114	61	<u>L38</u>
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<u>L36</u>	111 and 120	24	<u>L36</u>
<u>L35</u>	111 and 119	75	<u>L35</u>
<u>L34</u>	111 and 118	75	<u>L34</u>

<u>L33</u>	l11 and l17	8	<u>L33</u>
<u>L32</u>	l11 and l16	13	<u>L32</u>
<u>L31</u>	l11 and l15	22	<u>L31</u>
<u>L30</u>	l11 and l14	69	<u>L30</u>
<u>L29</u>	l11 and (l13)	3	<u>L29</u>
<u>L28</u>	(l3 or l4) and l20	224	<u>L28</u>
<u>L27</u>	(l3 or l4) and l19	562	<u>L27</u>
<u>L26</u>	(l3 or l4) and l18	562	<u>L26</u>
<u>L25</u>	(l3 or l4) and l17	318	<u>L25</u>
<u>L24</u>	(l3 or l4) and l16	93	<u>L24</u>
<u>L23</u>	(l3 or l4) and l15	186	<u>L23</u>
<u>L22</u>	(l3 or l4) and l14	478	<u>L22</u>
<u>L21</u>	(l3 or l4) and l13	52	<u>L21</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L20</u>	(711/206-221)[CCLS]	4401	<u>L20</u>
<u>L19</u>	(711/112-173,201-221)[CCLS]	27399	<u>L19</u>
<u>L18</u>	(711/112-173,201-221)![CCLS]	27399	<u>L18</u>
<u>L17</u>	(717/134-149)[CCLS]	3188	<u>L17</u>
<u>L16</u>	(712/209-213)[CCLS]	1175	<u>L16</u>
<u>L15</u>	(712/213-227)[CCLS]	4694	<u>L15</u>
<u>L14</u>	(712/2-300)[CCLS]	12828	<u>L14</u>
<u>L13</u>	(703/24-28)[CCLS]	1430	<u>L13</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L12</u>	L11 and l1	185	<u>L12</u>
<u>L11</u>	L10 and l2	233	<u>L11</u>
<u>L10</u>	cach\$3 near12 (fetch\$5 or prefetch\$5) near12 (skip\$5 or minimiz\$7 or reduc\$5 or eliminat\$5 or bypass\$3 or avoid\$3)	1773	<u>L10</u>
<u>L9</u>	cach\$3 near12 (fetch\$5 or prefetch\$5) and l4	7	<u>L9</u>
<u>L8</u>	cach\$3 near12 (fetch\$5 or prefetch\$5) and l3	852	<u>L8</u>
<u>L7</u>	cach\$3 near12 (fetch\$5 or prefetch\$5) near45 l2	4	<u>L7</u>
<u>L6</u>	(table\$1 or look\$4 near2 up) and l4	200	<u>L6</u>
<u>L5</u>	(table\$1 or look\$4 near2 up) and l3	13648	<u>L5</u>
<u>L4</u>	l1 near55 L2	385	<u>L4</u>
<u>L3</u>	l1 and L2	18504	<u>L3</u>
<u>L2</u>	(translat\$5 or convert\$5 or conversion\$1) near25 (skip\$5 or minimiz\$7 or reduc\$5 or eliminat\$5 or bypass\$3 or avoid\$3)	328950	<u>L2</u>
<u>L1</u>	(append\$4 or attach\$5 or tag\$4 or embed\$5 or insert\$5) near15 (cod\$3 or program\$5 or instruction\$1 or command\$3 or microinstruction\$1 or macro\$1 or macro near1 instruction\$1)	268670	<u>L1</u>

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IEEE JNL IEEE Journal or Magazine

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IEEE CNF IEEE Conference Proceeding

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Kotz, D.F.; Ellis, C.S.;

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Volume 1, Issue 2, April 1990 Page(s):218 - 230

Digital Object Identifier 10.1109/71.80133

[AbstractPlus](#) | Full Text: [PDF](#)(1284 KB) IEEE JNL[Rights and Permissions](#)**2. A tile size selection analysis for blocked array layouts**

Athanasaki, E.; Koziris, N.; Tsanakas, P.;

[Interaction between Compilers and Computer Architectures, 2005. INTERACT-9. 9th Annual Works](#)

13 Feb. 2005 Page(s):70 - 80

Digital Object Identifier 10.1109/INTERACT.2005.1

[AbstractPlus](#) | Full Text: [PDF](#)(352 KB) IEEE CNF[Rights and Permissions](#)**3. Compiler-driven cached code compression schemes for embedded ILP processors**

Larin, S.Y.; Conte, T.M.;

[Microarchitecture, 1999. MICRO-32. Proceedings. 32nd Annual International Symposium on](#)

16-18 Nov. 1999 Page(s):82 - 92

Digital Object Identifier 10.1109/MICRO.1999.809446

[AbstractPlus](#) | Full Text: [PDF](#)(120 KB) IEEE CNF[Rights and Permissions](#)**4. Operating system support for small objects**

Wilson, P.R.;

[Object Orientation in Operating Systems, 1991. Proceedings. 1991 International Workshop on](#)

17-18 Oct. 1991 Page(s):80 - 86

Digital Object Identifier 10.1109/IWOOS.1991.183026

[AbstractPlus](#) | Full Text: [PDF](#)(516 KB) IEEE CNF[Rights and Permissions](#)**5. Enhancing loop buffering of media and telecommunications applications using low-overhea**

Sias, J.W.; Hunter, H.C.; Hwu, W.-M.W.;

[Microarchitecture, 2001. MICRO-34. Proceedings. 34th ACM/IEEE International Symposium on](#)

1-5 Dec. 2001 Page(s):262 - 273

Digital Object Identifier 10.1109/MICRO.2001.991124

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